

IN THE CLAIMS

1. - 10. (Canceled)

11. (Currently Amended) ~~A substrate structure which is a precursor to~~  
[[an]] An electron source[[,]] comprising:  
a substrate; [[and]]  
[[an]] a first insulating material film provided directly on [[said]] the  
substrate, wherein [[said]] the first insulating material film includes a plurality of metallic  
oxide particles, [[and]] ~~has a vacancy provided among said plurality of vacancies formed~~  
between the [[of]] metallic oxide particles, and ~~said insulating material film has a surface~~  
~~on which a member for electron emission of the electron source is to be disposed~~ has a  
ratio of said vacancy in its cross section within a range of 5-10%;  
a second insulating material film provided directly on the first  
insulating material film;  
a pair of electrodes provided on the second insulating material film;  
a pair of electroconductive films provided between the pair of  
electrodes and connected respectively to the pair of electrodes; and  
a carbon film provided on at least one electroconductive film of the  
pair of electroconductive films.

12. (Canceled)

13. (Currently Amended) The substrate structure according to claim 11 [[or 12]], wherein a thickness of said first insulating material film is within the range of 150 nm to 3  $\mu\text{m}$ .

14. (Currently Amended) The substrate structure according to ~~any one of claims~~ claim 11[[ or 12]], wherein said first insulating material film further contains phosphorus.

15. (Currently Amended) The substrate structure according to ~~any one of claims~~ claim 11[[ or 12]], wherein an insulating material of said first insulating material film is  $\text{SiO}_2$ .

16. (Canceled)

17. (Currently Amended) The substrate structure according to claim 11 ~~16~~, wherein a thickness of the second insulating material film ~~made of said insulating material~~ is within the range of 20 nm to 3  $\mu\text{m}$ .

18. (Currently Amended) The substrate structure according to claim 11 ~~16~~, wherein on said first insulating material film, the second insulating material film is laminated, and the second insulating film includes  $\text{SiO}_2$ .

19-22. (Canceled)

23. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein an average particle size of said plurality of metallic oxide particles is within the range of 6 nm to 60 nm.

24. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein an average particle size of said plurality of metallic oxide particles is within the range of 6 nm to 20 nm.

25. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein the size of said vacancies formed between the metallic oxide particles ~~said vacancy~~ is within the range of 0.1 to 5 times an average particle size of said plurality of metallic oxide particles.

26. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein the size of said vacancies formed between the metallic oxide particles ~~said vacancy~~ is within the range of 0.1 to 2 times an average particle size of said plurality of metallic oxide particles.

27. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein said metallic oxide particles are electronically conductive particles.

28. (Currently Amended) The substrate structure according to claim 11 [[or 19]], wherein said metallic oxide particles are particles of  $\text{SnO}_2$ .

29. (Currently Amended) The substrate structure according to ~~any one of claims 1, claim 11 or 19~~, wherein said substrate is a substrate containing sodium.

30. (Currently Amended) The substrate structure according to claim 29, wherein said first and second insulating material films are ~~film is a~~ sodium blocking films ~~film~~.

31. (Currently Amended) The substrate structure according to ~~any one of claims 1, claim 11 or 19~~, wherein said first and second insulating material films are ~~film is~~ an antistatic films ~~film~~.

32. - 34. (Canceled)

35. (Currently Amended) An image display apparatus comprising ~~at least one member for electron emission, an image display member for displaying images by~~

~~irradiation of at least one electron from said member for electron emission, and an envelope in which said member for electron emission and said image display member are arranged, wherein a substrate where said member for electron emission is arranged is a substrate structure according to any one of claims 1, 11 or 19~~ an electron source and an image display member disposed in opposition to the electron source, wherein the electron source is an electron source according to claim 11.

36. (Canceled)

37. (Currently Amended) The image display apparatus according to claim 35, wherein said electron source ~~the at least one member for electron emission~~ includes a plurality of electron-emitting devices matrix-wired by a plurality of row-directional wirings and a plurality of column-directional wirings.